

**AMENDMENTS TO THE SPECIFICATION**

**Please replace the paragraph beginning at page 49, line 8, with the following:**

Since the surface side of the bank switching is the CPU-use memory bank 8008, the CPU 8001 takes out a process 8105 from the CPU-use memory bank 8008. Since the process 8105, which has the same function as the process 8102 containing commands that use the DSP 8002, contains commands that use the CPU 8001, the CPU 8001 executes the corresponding process. In the case when, upon completion of the process 8105, the sequence returns to the main function of the program 8101 that is the calling end of process func1, instantaneously as the command used for returning to the main function is fetched by the CPU 8001, the bank-switching device 8010 carries out an unlocking operation on the bank-switching process. Then, the bank-switching request to the memory 8006 is accepted, and consequently, the ~~DSP-use memory bank 8007~~ CPU-use memory bank 8008 is placed on the surface side with ~~CPU-use memory bank 8008~~ DSP-use memory bank 8007 being placed on the rear side. In the case when, while the CPU 8001 is executing the process 8105, the other process using the DSP 8002 has been finished, upon receipt of the output of the monitoring device 8009 for the status of use, the bank-switching device 8010 issues a request for placing the DSP-use memory bank 8007 on the surface side of the bank switching, while placing the CPU-use memory bank 8008 on the rear side. Here, in the same manner as described earlier, this request is received and the bank switching is carried out, after the unlocking process for the bank switching has been carried out.

**Please replace the paragraph beginning at page 50, line 14, with the following:**

Since the surface side of the bank switching is the CPU-use memory bank 8008, the CPU 8001 takes out a process 8106 from the CPU-use memory bank 8008. Since the process 8106, which has the same function as the process 8103 containing commands that use the DSP 8002, contains commands that use the CPU 8001, the CPU 8001 executes the corresponding process. In the case when, upon completion of the process 8106, the sequence returns to the main function of the program 8101 that is the calling end of process func2, instantaneously as the command used for returning to the main function is fetched by the CPU 8001, the bank-switching device 8010 carries out an unlocking operation on the bank-switching process. Then, the bank-switching request to the memory 8006 is accepted, and consequently, the ~~DSP-use memory bank 8007~~ CPU-use memory bank 8008 is placed on the surface side with ~~CPU-use memory bank 8008~~ DSP-use memory bank 8007 being placed on the rear side. In the case when, while the CPU 8001 is executing the process 8106, the command in the process 8102 using the DSP 8002 has been finished, in the same manner as the previous case in which upon executing the processing of process func1, the DSP 8002 is being used, the bank-switching device 8010 issues a request for bank switching to the memory 8006, and this request is accepted after the unlocking for the bank switching has been executed.